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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/626,438	07/24/2003	Andrew Marshall	TI-34177	9616	
23494	7590 01/14/2005	EXAMINER			
	STRUMENTS INCOR	LE, THON	LE, THONG QUOC		
P O BOX 655474, M/S 3999 DALLAS, TX 75265			ART UNIT	PAPER NUMBER	
,			2818		

DATE MAILED: 01/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	No.	Applicant(s)				
Office Action Summary		10/626,438	:	MARSHALL, ANDREW				
		Examiner	,	Art Unit				
		Thong Q. Le		2818				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).								
Status								
1)	Responsive to communication(s) filed on							
2a) <u></u>	This action is FINAL . 2b)⊠ Thi	is action is no	n-final.					
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims								
5)□ 6)⊠ 7)⊠	 4) Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-6,8-16 and 18-20 is/are rejected. 7) Claim(s) 7 and 17 is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement. 							
Applicati	on Papers							
 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 								
Priority under 35 U.S.C. § 119								
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 								
2) Notice 3) Information	et(s) De of References Cited (PTO-892) De of Draftsperson's Patent Drawing Review (PTO-948) De of Draftsperson's Patent Drawing Review (PTO-948) De of Draftsperson's Patent (s) (PTO-1449 or PTO/SB/06) De of No(s)/Mail Date	18)	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal F 6) Other:	ate	O-152)			

Application/Control Number: 10/626,438 Page 2

Art Unit: 2818

DETAILED ACTION

1. Amendment filed on 12/22/2004 has been entered.

2. Claims 1-20 are presented for examination.

Response to Amendment

3. The reply filed on 12/22/2004 is not fully responsive because it fails to include a complete or accurate record of the substance of the amendment.

Applicant is asked to submit a copy of amendment.

Response to Arguments

4. Applicant's arguments with respect to claims 1-20 have been considered but are moot in view of the new ground(s) of rejection.

Drawings

5. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.121(d)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Page 4, lines 8-9 in specification, Figure 1 is a prior art.

Application/Control Number: 10/626,438 Page 3

Art Unit: 2818

6. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: labels 200, 202, 204 in Figure 2. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

7. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, "the memory unit is coupled between Vdd and Vddinternal terminals in claim 8 and the memory unit is coupled between a first vddinternal and a second vdd internal terminal" must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet,

Art Unit: 2818

and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Regarding to drawings, there is no drawing has memory unit is arranged as disclosed in claims 8-9.

Claim Rejections - 35 USC § 112

- 8. The following is a quotation of the second paragraph of 35 U.S.C. 112:

 The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 9. Claims 8-9, 18-19 are rejected under 35 U.S.C. 112, second paragraph, as failing to set forth the subject matter which applicant(s) regard as their invention. Evidence that claims 8-9 fail(s) to correspond in scope with that which applicant(s) regard as the invention can be found in the drawings. In the drawings paper, applicant has stated memory unit coupled between Vddinternal and Vss, and this statement indicates that the invention is different from what is defined in the claim(s) because no drawing has memory unit is arranged between Vdd and vddinternal or between vddinternal terminals as defined in claims 8-9, 18-19.

10. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 11. Claims 1-6, 10-16, 20 are rejected under 35 U.S.C. 102(b) as being anticipated by Manning (U.S. Patent No. 5,684,751).

Regarding claims 1-6, 11-16, Manning discloses a circuit (Figure 3) for reducing standby leakage in a memory unit, comprising:

a capacitive divider (Figure 3, C1) coupled to the memory unit (Column 8, lines 30-32) so as to generate a voltage across the memory unit, the voltage being adequate to retain memory values during one of a sleep state and a standby state (Column 7, lines 66-67, Column 8, lines 1-6, Column 6, lines 29-36), and wherein said capacitive divider is coupled to the memory unit on-chip (Figures 2-3), and wherein the voltage is a division of a normal operating voltage (Column 8, lines 2-6), and wherein the voltage is substantially Vdd/2 (Column 8, lines 2-6), and wherein the voltage is substantially Vdd/3 (Column 9, lines 40-55, array voltage), and wherein said capacitive divider is configured for varying an oscillator frequency in accordance with the generated voltage so as to minimize switching losses (ABSTRACT, Column 2, lines 47-60).

Regarding claims 10,20, Manning discloses an integrated circuit for reducing standby leakage in a memory unit (Figure 4), comprising: a substrate (310) having a

capacitive divider (Figure 5, C1) coupled to the memory unit so as to generate a voltage across the memory unit, the voltage being adequate to retain memory values during one of a sleep state and a standby state.

Allowable Subject Matter

12. Claims 7, 17 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 7,17 include allowable subject matter since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. Manning (U.S. Patent No. 5,684,751), and others, does not teach the claimed invention having a memory unit is coupled between Vss and vddinternal terminals.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thong Q. Le whose telephone number is 571-272-1783. The examiner can normally be reached on 8:00am-5:00pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms can be reached on 571-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 10/626,438 Page 7

Art Unit: 2818

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Thong Q. Le Primary Examiner Art Unit 2818

Thoyk

THONG LES.
PRIMARY EXAMINER